

DIVISION

10/553 739  
20 NOV 06  
DPP REF

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hideyuki OKUMA**

20 NOV 21 PH 4: Group Art Unit: 2614

Application Number: **10/553,739**

Examiner:

Filed: **October 20, 2005**

Confirmation Number: **7770**

For: **ANALOG/DIGITAL-COMPATIBLE FRONT-END MODULE**

Attorney Docket Number: **052972**  
Customer Number: **38834**

REQUEST FOR REFUND

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

November 20, 2006

Sir:

The undersigned respectfully requests a refund in the amount of \$360.00 charged in error to our deposit account No. 50-2866 for the above-identified patent application. A copy of the Deposit Account monthly statement of June 2006 accompanies this Request.

The application was filed on October 20, 2005, together with a Preliminary Amendment (copy enclosed) removing the multiple dependency of the claims. The total number of claims was 7 and the proper check for these claims was \$900.00. This amount was paid when the application was filed on October 20, 2005. A copy of the post-card indicating the filing of the preliminary amendment is also enclosed.

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Request for Refund  
Application No. 10/553,739  
Attorney Docket No. 052972

Applicants request that the Patent Office refund the amount of \$360.00 to Deposit  
Account 50-2866:

Respectfully submitted,

**WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP**



Stephen G. Adrian

Registration No. 32,878  
Telephone: (202) 822-1100  
Facsimile: (202) 822-1111

SGA/yap

Enclosures: Deposit Account Monthly Statement of June 2006  
Copy of the Preliminary Amendment, and  
Copy of Post-Card of October 20, 2005



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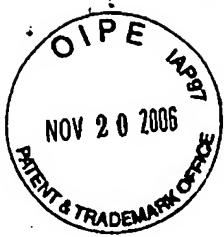
**Deposit Account Statement**

**Requested Statement Month:** June 2006  
**Deposit Account Number:** 502866  
**Name:** WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP  
**Attention:** ACCOUNTS PAYABLE  
**Address:** 1250 CONNECTICUT AVE, NW  
**City:** WASHINGTON  
**State:** DC  
**Zip:** 20036  
**Country:** UNITED STATES

DATE SEQ	REF	POSTING TXT	ATTORNEY NBR	DOCKET	FEE CODE	AMT	BAL
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06/09 37		10206351			9204	-\$455.00	\$2,461.00
06/12 21		10553739	052972		1616	\$360.00	\$2,101.00
06/12 60		10416169	030537		1202	-\$250.00	\$2,351.00
06/13 10		10781811			9204	-\$1,709.00	\$4,060.00
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06/30 18		11401885	062399		1053	\$130.00	\$4,175.00

START BALANCE	SUM OF CHARGES	SUM OF REPLENISH	END BALANCE
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U.S. Patent Application

Docket No: 052972

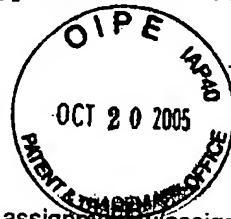
Serial No.: New application w/declaration  
(§371 of international application No. PCT/JP2004/016294)  
Filed: October 20, 2005

Applicant(s): Hideyuki OKUMA

Papers filed herewith on: October 20, 2005

FEE: \$940.00

Other: New appn- 17 pgs; drwgs- 3 sheets; assignment/w/assign.record.cover sheet;  
Prel. Amd.; IDS/PTO/SB/08 w/6 refs. & Int'l Search Report



COMMISSIONER OF PATENTS

Receipt is hereby acknowledged of the papers filed as indicated  
in connection with the above-identified case.

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3386-0108

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**RECEIVED  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Hideyuki OKUMA**

Serial Number: **Not yet assigned**  
(§371 of international application No. PCT/JP2004/016294)

Filed: **October 20, 2005**

For: **ANALOG/DIGITAL-COMPATIBLE FRONT-END MODULE**

Attorney Docket Number: **052972**  
Customer Number: **38834**

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 20, 2005

Sir:

Prior to calculation of the filing fee and examination of this application, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 5 of this paper.

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**AMENDMENTS TO THE CLAIMS**

The following listing of claims replaces all prior versions of claims in the application.

1. (Original) An analog/digital-compatible front-end module capable of receiving analog television broadcast and digital television broadcast, the analog/digital-compatible front-end module comprising a high frequency amplifier 9 for amplifying a high frequency signal received by an antenna 1; a frequency converter circuit for frequency-converting an output signal from the high frequency amplifier 9 to output an intermediate frequency signal; an analog demodulator 5 for receiving analog television broadcast; a digital demodulator 6 for receiving digital television broadcast; analog/digital switching means for selectively feeding an output signal from the frequency converter circuit to the analog demodulator 5 or to the digital demodulator 6; and gain control switching means for controlling gain of the high frequency amplifier 9 in accordance with a gain control signal obtained from the analog demodulator 5 during an analog broadcast reception, while controlling gain of the high frequency amplifier 9 in accordance with a gain control signal produced from the output signal from the frequency converter circuit during a digital broadcast reception.
  
2. (Original) An analog/digital-compatible front-end module according to claim 1, wherein a wideband RF filter is interposed between the high frequency amplifier 9 and the frequency converter circuit, and an IF filter with a narrower band than that of the RF filter is interposed between the frequency converter circuit and the analog/digital switching means.

3. (Currently Amended) An analog/digital-compatible front-end module according to claim 1 [[or 2]], wherein the frequency converter circuit comprises an intermediate frequency amplifier 14 for amplifying the intermediate frequency signal frequency-converted, and during the digital broadcast reception an output signal from the intermediate frequency amplifier 14 is detected and selected by the gain control switching means, and is fed for gain control of the high frequency amplifier 9.

4. (Currently Amended) An analog/digital-compatible front-end module according to ~~any one of claims 1 to 3~~ claim 1, wherein the gain control switching means comprises a signal switch 7 for selecting the gain control signal produced from the output signal from the frequency converter circuit or the gain control signal obtained from the analog demodulator 5 to feed the selected signal to the high frequency amplifier 9.

5. (New) An analog/digital-compatible front-end module according to claim 2, wherein the frequency converter circuit comprises an intermediate frequency amplifier 14 for amplifying the intermediate frequency signal frequency-converted, and during the digital broadcast reception an output signal from the intermediate frequency amplifier 14 is detected and selected by the gain control switching means, and is fed for gain control of the high frequency amplifier 9.

6. (New) An analog/digital-compatible front-end module according to claim 2,  
wherein the gain control switching means comprises a signal switch 7 for selecting the gain control  
signal produced from the output signal from the frequency converter circuit or the gain control  
signal obtained from the analog demodulator 5 to feed the selected signal to the high frequency  
amplifier 9.

7. (New) An analog/digital-compatible front-end module according to claim 3,  
wherein the gain control switching means comprises a signal switch 7 for selecting the gain control  
signal produced from the output signal from the frequency converter circuit or the gain control  
signal obtained from the analog demodulator 5 to feed the selected signal to the high frequency  
amplifier 9.

Preliminary Amendment  
Attorney Docket No. 052972

REMARKS

The above amendment to the claims has been made to correct errors, to remove the multiple dependency of the claims and to place the application in better condition for examination.

If any fees are due in connection with this paper, please charge our Deposit Account No.  
50-2866.

Respectfully submitted,  
**WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP**

  
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